

**AMENDMENTS TO THE CLAIMS:**

*Please amend the claims as follows:*

1. (Currently Amended) A structure of a power supply path utilized in the design of an integrated circuit, wherein in every cell in the integrated circuit, a power supply path on a power supply side of a high potential and a power supply path on a power supply side of a low potential are provided opposite each other,  
and

wherein in every cell in the integrated circuit, the power supply path on the power supply side of the high potential and the power supply path on the power supply side of the low potential ~~and the power supply side of a potential~~ each comprise:

a main power line; and

a plurality of outgoing power lines branching off from the main power line,

wherein a plurality of pitches between adjacent outgoing power lines of the plurality of branched outgoing lines in the longitudinal direction of the main line are set so as to be equal to each other, and

the pitch between the main power line of the power supply path on the power supply side of the high potential and the main power line of the power supply path on the power supply side of the low potential is set to be longer than the sum of the length of an outgoing line of the power supply side of the high potential and the length of an outgoing line of the power supply side of the low potential provided oppositely to said outgoing line of the power supply side of the high potential.

2. (Previously presented) The structure of a power supply path utilized in the design of the integrated circuit according to claim 1, wherein branching positions of the plurality of outgoing power lines of the power supply path on the power supply side of the high potential correspond to branching positions of the plurality of outgoing power lines of the power supply path on the power supply side of the low potential in the longitudinal direction of the power supply paths.

3. (Previously presented) The structure of a power supply path utilized in the design of the integrated circuit as in claim 1, wherein lengths of the respective plurality of outgoing power lines are set so as to be equal to each other in both the power supply paths on the power supply sides of the high potential and the low potential, respectively.

4. (Previously presented) The structure of a power supply path utilized in the design of the integrated circuit as in claim 2, wherein lengths of the respective plurality of outgoing power lines are set so as to be equal to each other in both the power supply paths on the power supply sides of the high potential and the low potential, respectively.

5. (Previously presented) The structure of a power supply path utilized in the design of the integrated circuit as in claim 3, wherein lengths of the plurality of outgoing power lines of the power supply path on the power supply side of the high potential are set so as to be longer than the lengths of the plurality of outgoing power lines of the power supply path on the power supply side of the low potential.

6. (Previously presented) The structure of a power supply path utilized in the design of the integrated circuit as in claim 4, wherein the lengths of the plurality of outgoing power lines of the power supply path on the power supply side of the high potential are set so as to be longer

than the lengths of the plurality of outgoing power lines of the power supply path on the power supply side of the low potential.

7. (Previously presented) The structure of a power supply path utilized in the design of the integrated circuit as in claim 1, wherein widths of the respective plurality of outgoing power lines are equal to each other and set so as to be smaller than distances between the adjacent outgoing power lines of both the power supply paths on the power supply sides of the high potential and the low potential, respectively.

8. (Previously presented) The structure of a power supply path utilized in the design of the integrated circuit as in claim 2, wherein widths of the respective plurality of outgoing power lines are equal to each other and set so as to be smaller than distances between the adjacent outgoing power lines of both the power supply paths on the power supply sides of the high potential and the low potential, respectively.

9. (Previously presented) The structure of a power supply path utilized in the design of the integrated circuit as in claim 3, wherein widths of the respective plurality of outgoing power lines are equal to each other and set so as to be smaller than distances between the adjacent outgoing power lines of both the power supply paths on the power supply sides of the high potential and the low potential, respectively.

10. (Previously presented) The structure of a power supply path utilized in the design of the integrated circuit as in claim 4, wherein widths of the respective plurality of outgoing power lines are equal to each other and set so as to be smaller than distances between the adjacent outgoing power lines of both the power supply paths on the power supply sides of the high potential and the low potential, respectively.

11. (Previously presented) The structure of a power supply path utilized in the design of the integrated circuit as in claim 5, wherein widths of the respective plurality of outgoing power lines are equal to each other and set so as to be smaller than distances between the adjacent outgoing power lines of both the power supply paths on the power supply sides of the high potential and the low potential, respectively.

12. (Previously presented) The structure of a power supply path utilized in the design of the integrated circuit as in claim 6, wherein widths of the respective plurality of outgoing power lines are equal to each other and set so as to be smaller than distances between the adjacent outgoing power lines of both the power supply paths on the power supply sides of the high potential and the low potential, respectively.

13. (Cancelled)

14. (Previously presented) The structure of a power supply path utilized in design of the integrated circuit as in claim 1, wherein the pitches between adjacent outgoing power lines of the power supply path on the power supply side of the high potential and the pitches between adjacent outgoing power lines of the power supply path on the power supply side of the low potential are set so as to be equal to each other.

15. (Cancelled)